

1/10

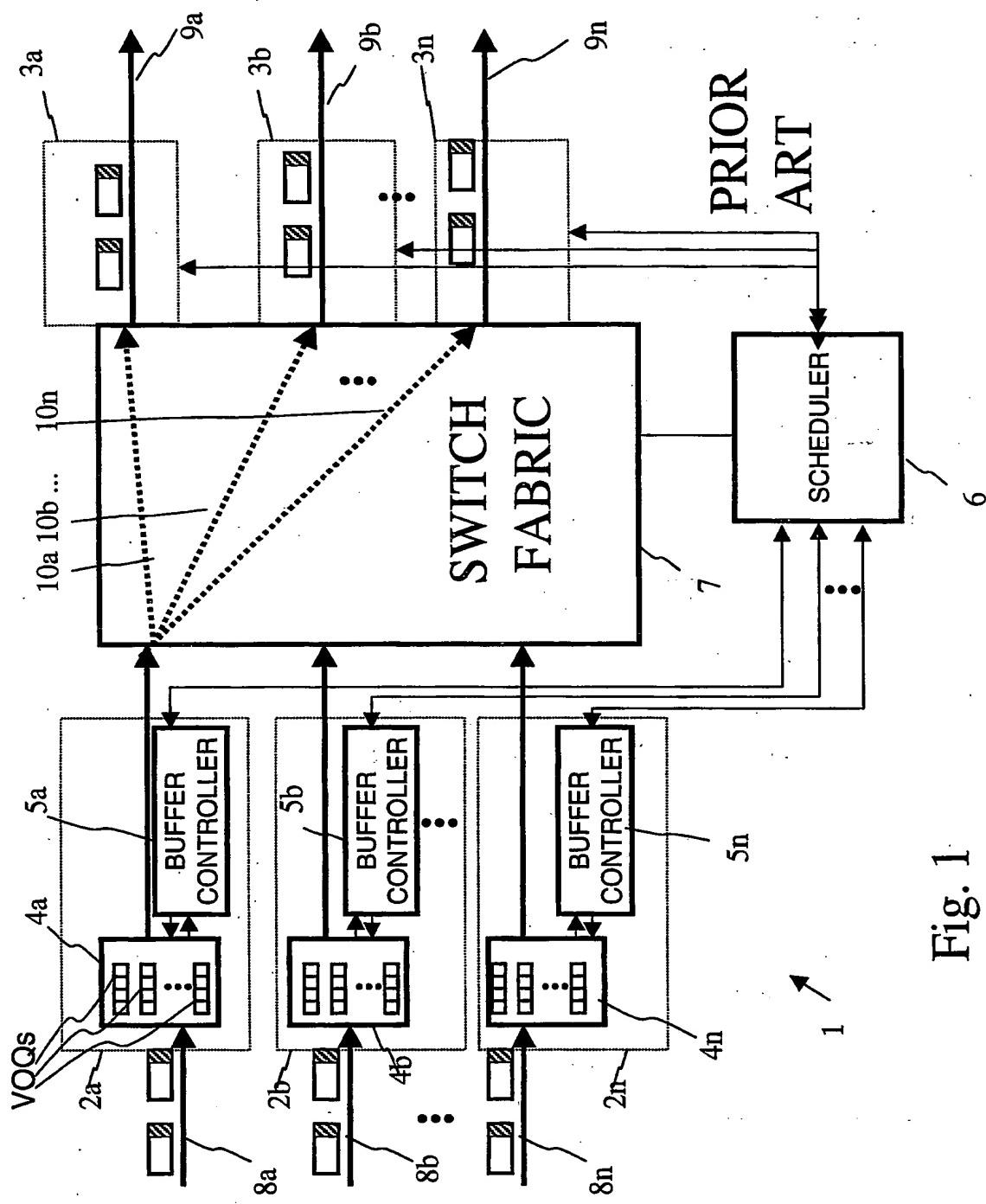
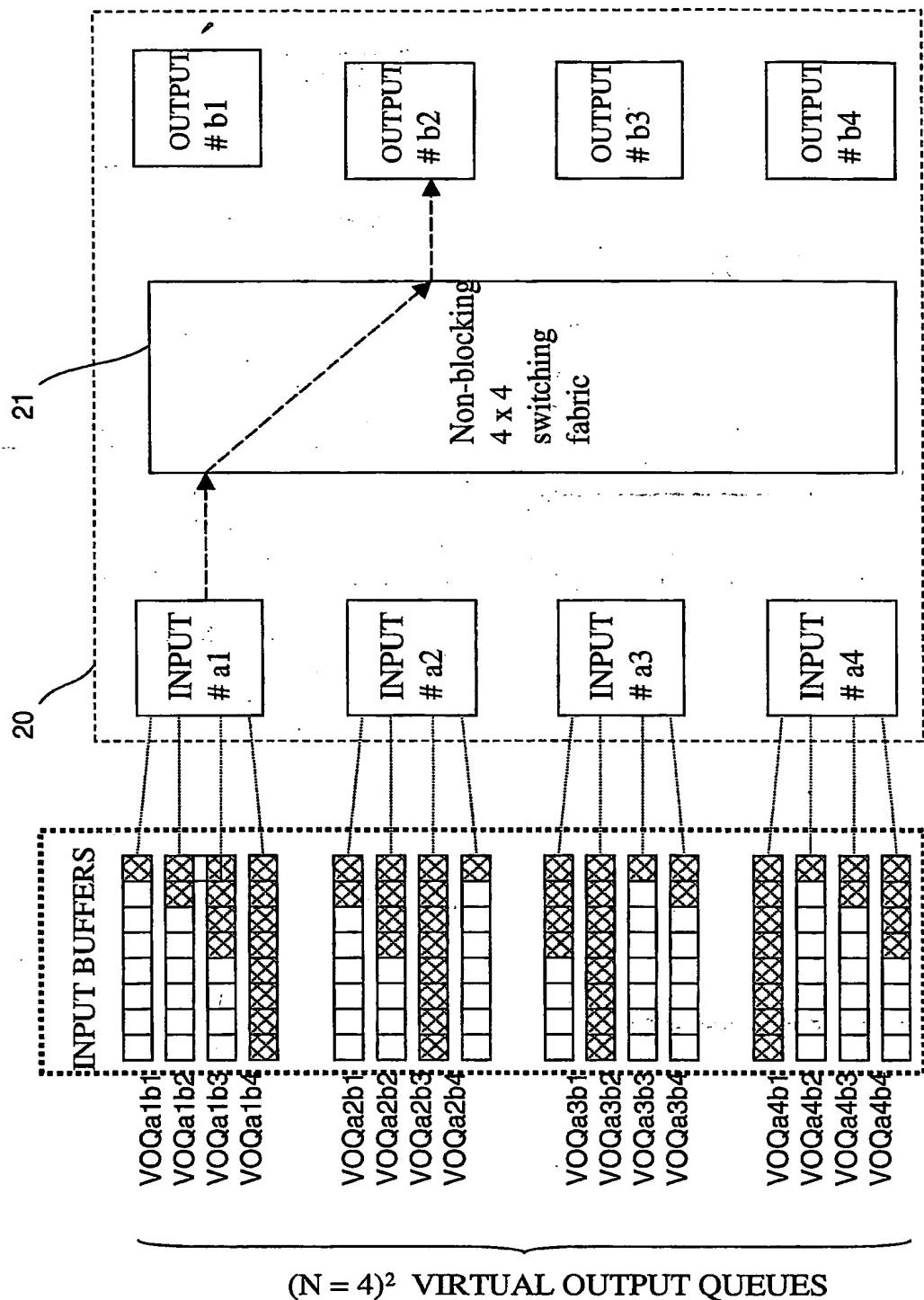


Fig. 1

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PRIOR ART

Fig. 2

$$\begin{bmatrix} 1 & 2 & 4 & 8 \\ 2 & 4 & 8 & 1 \\ 4 & 8 & 1 & 2 \\ 8 & 1 & 2 & 4 \end{bmatrix}$$

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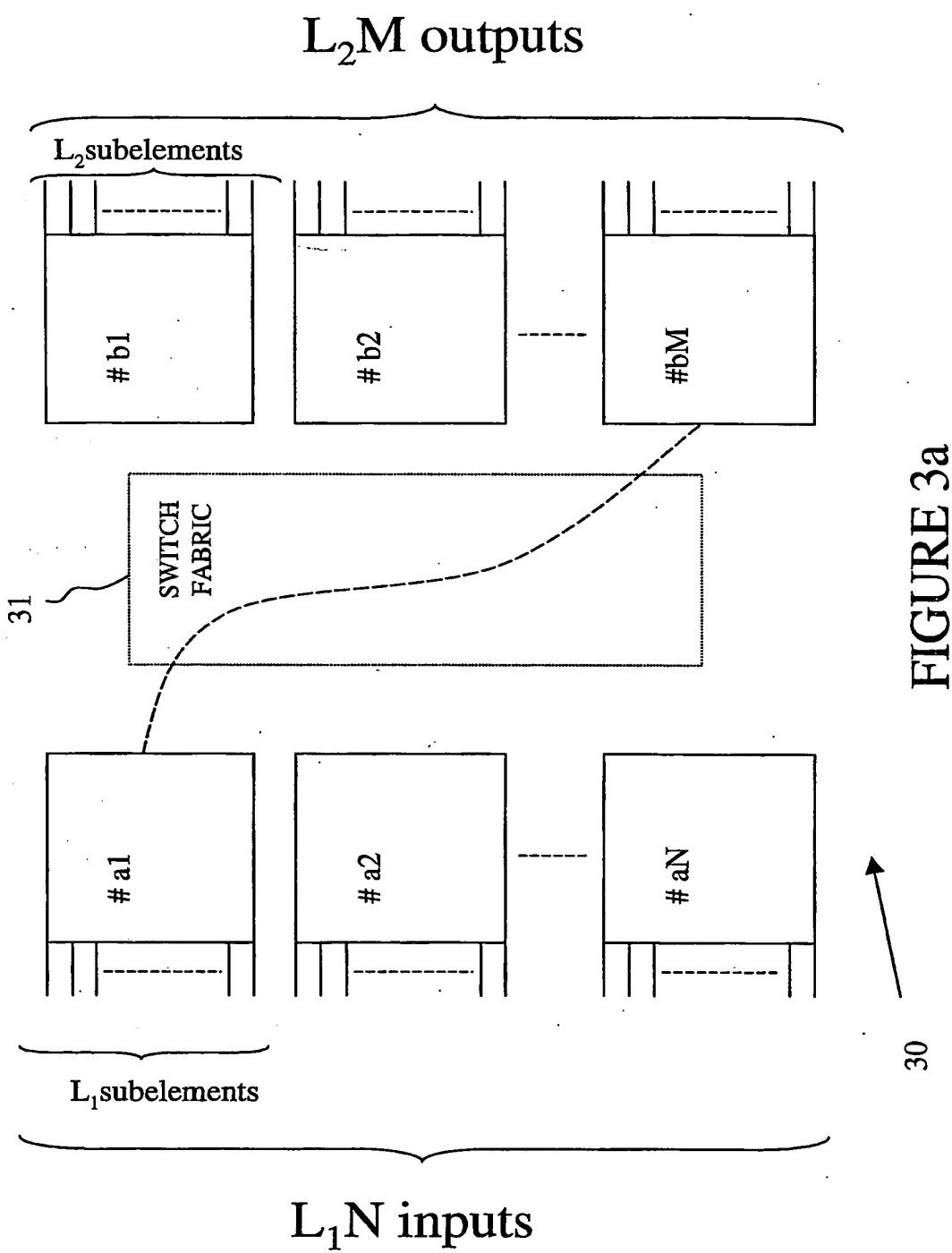


FIGURE 3a

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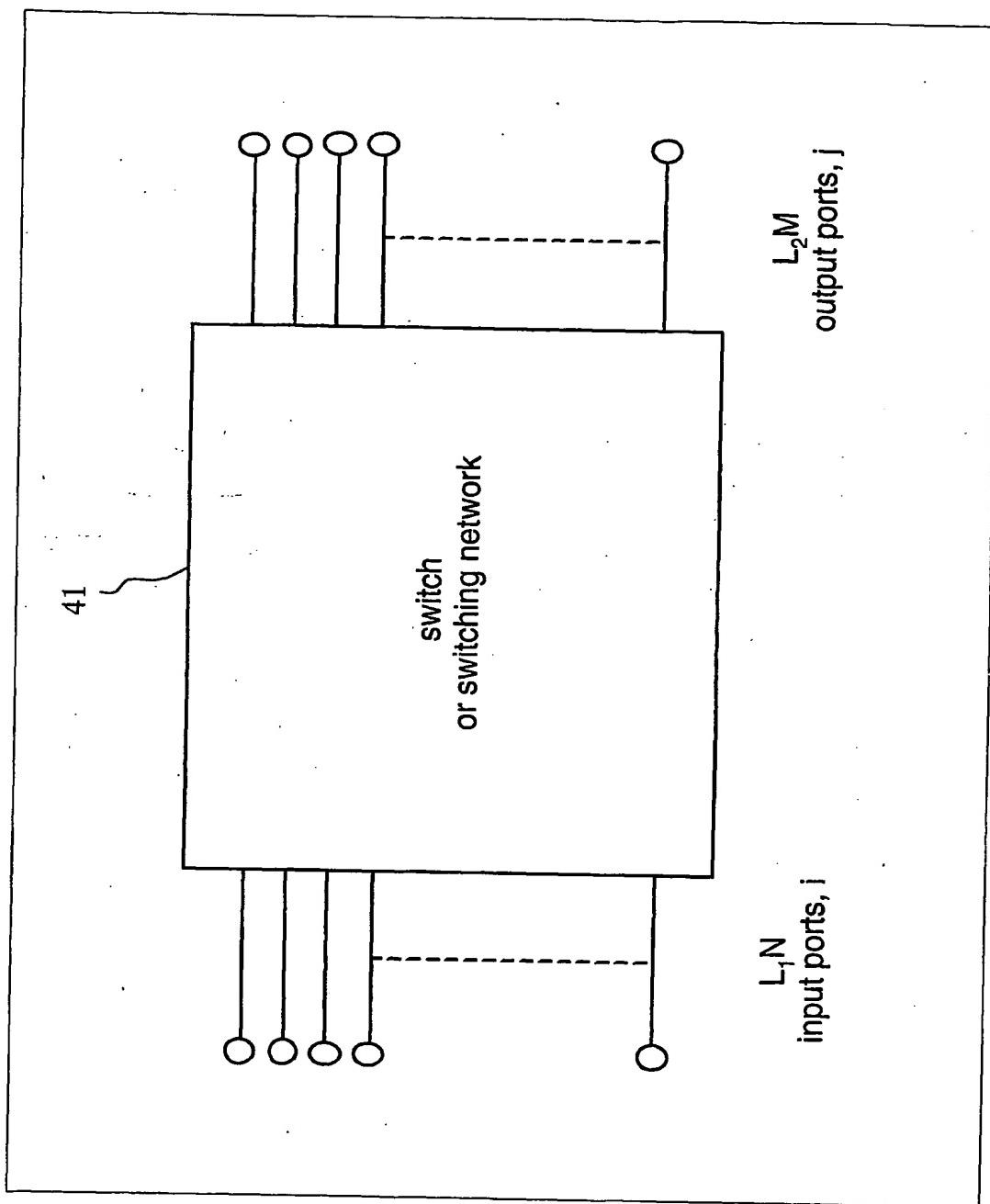


FIG. 3b

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## (UNPOPULATED)SYMMETRIC REQUEST MATRIX


 $L_1 N$   
inputs, i $L_2 M$  outputs, j

FIG 3c

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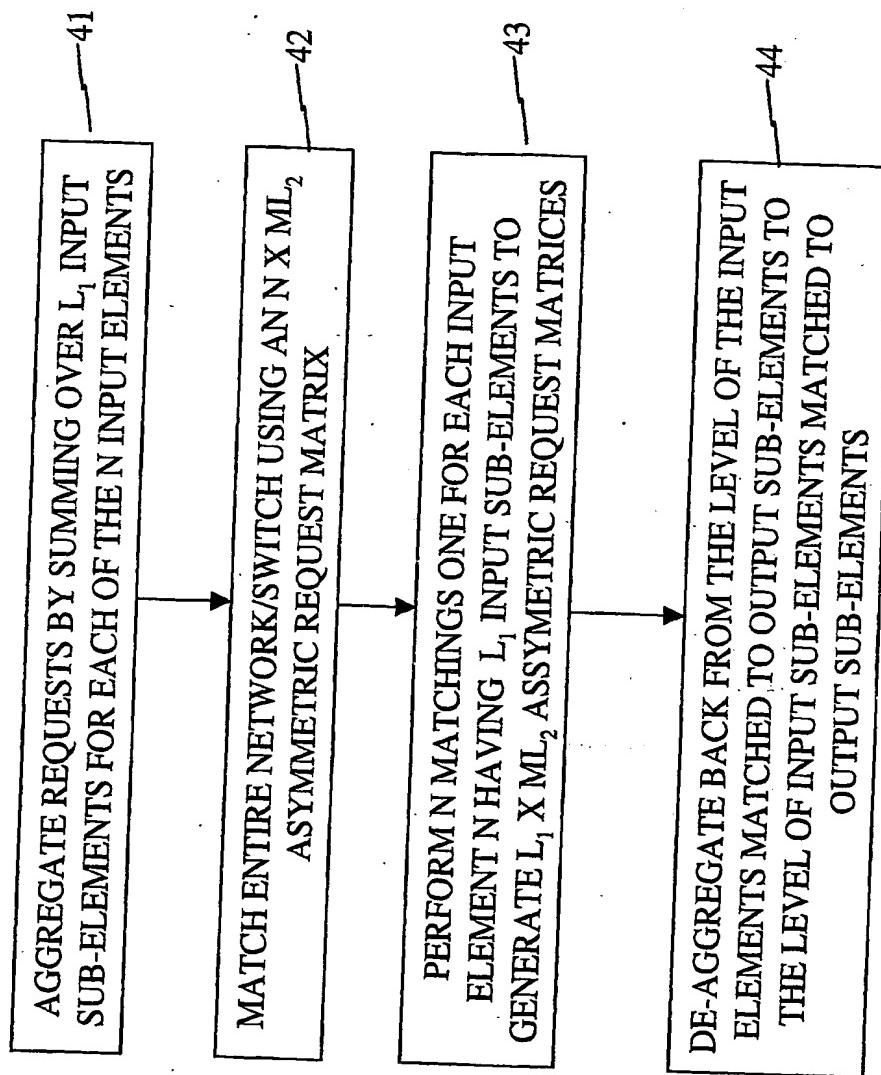


FIGURE 4

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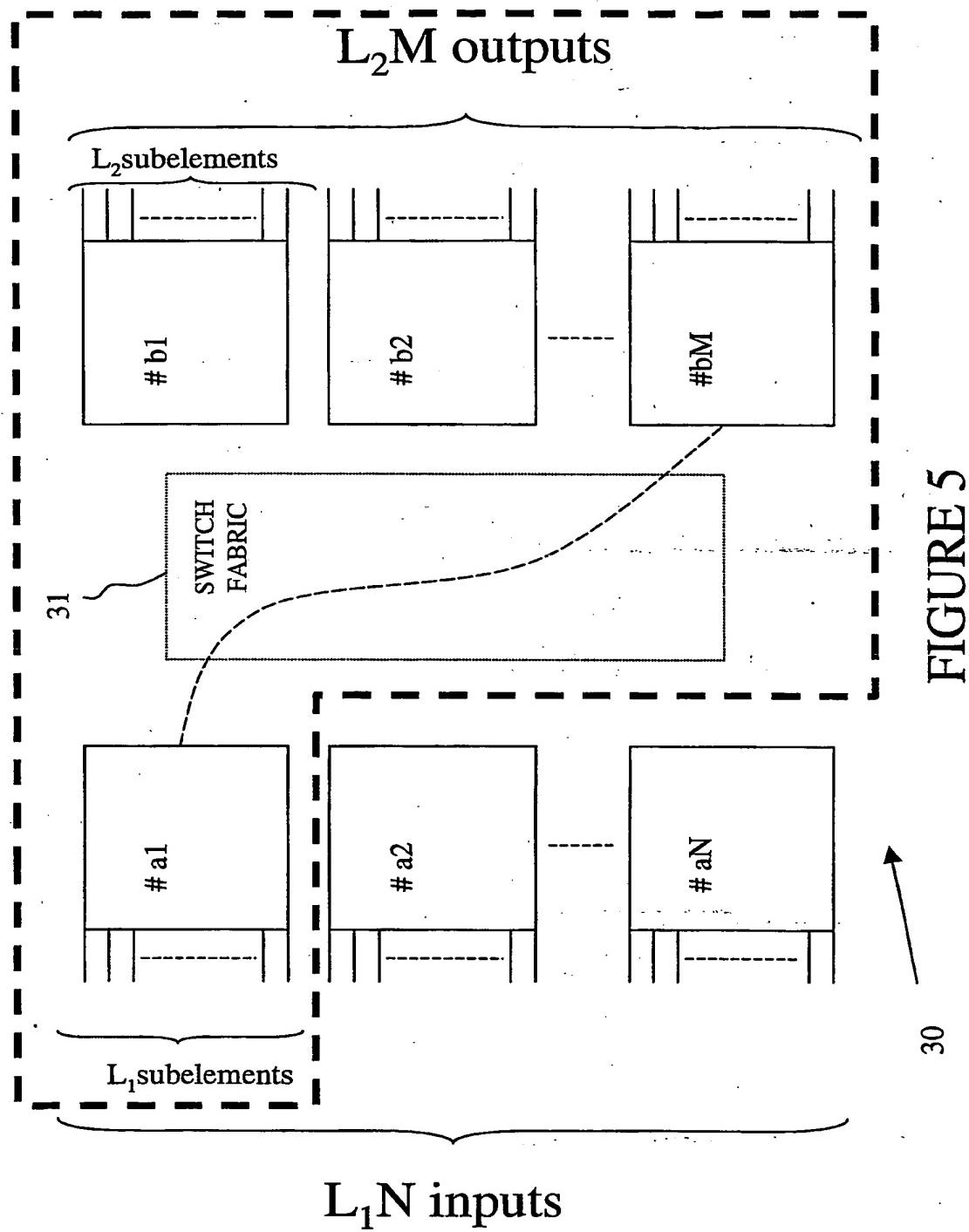


FIGURE 5

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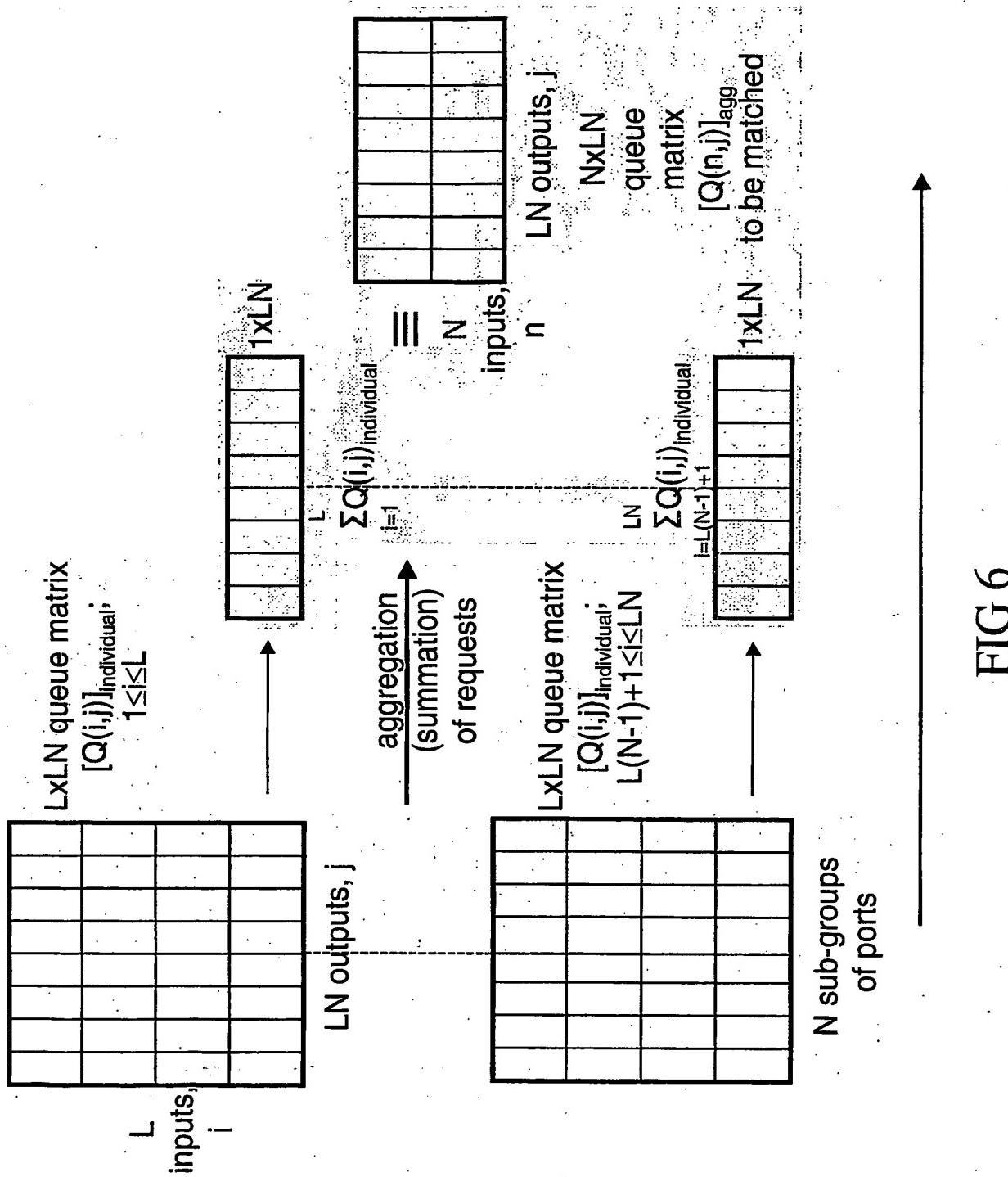


FIG 6

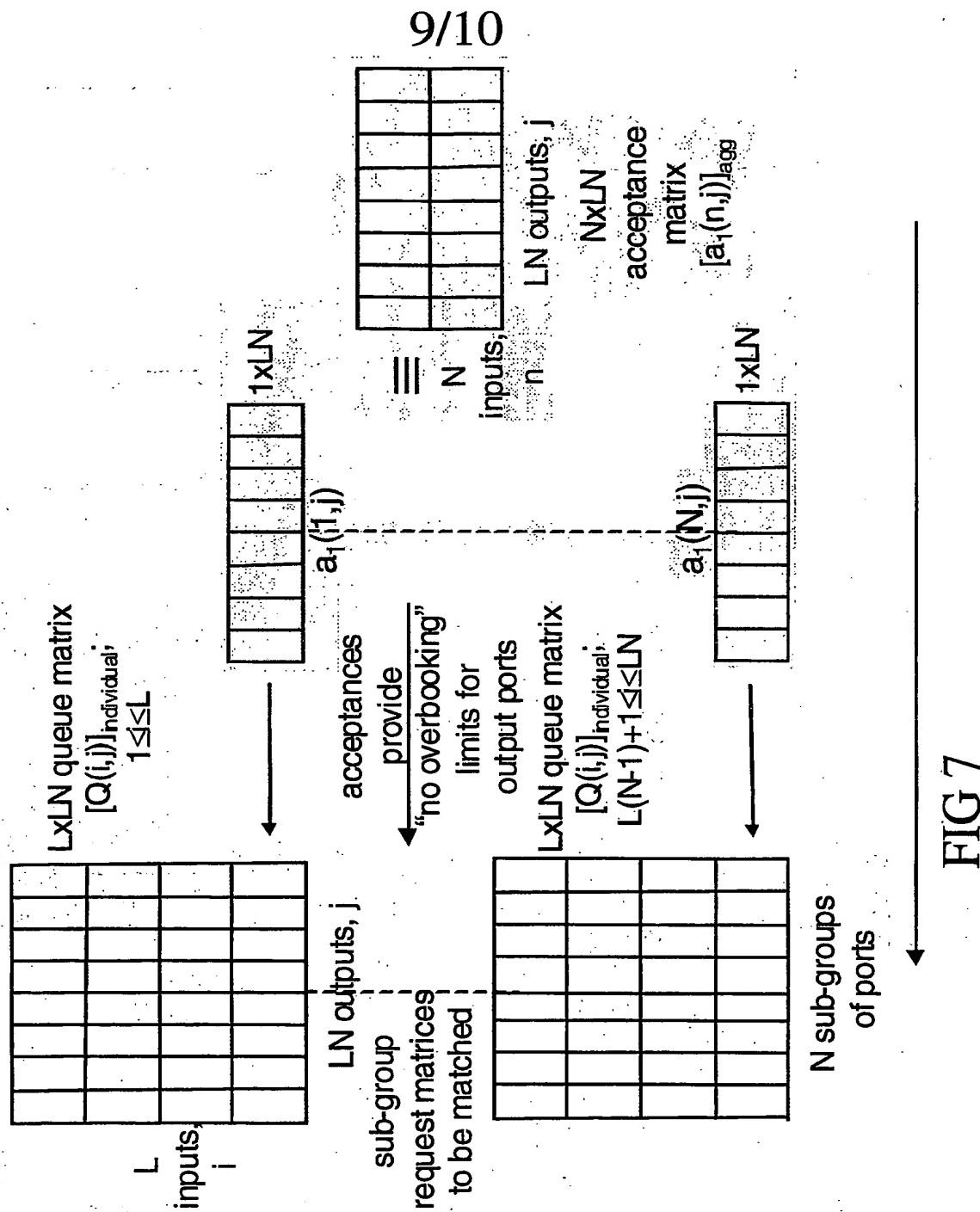


FIG 7

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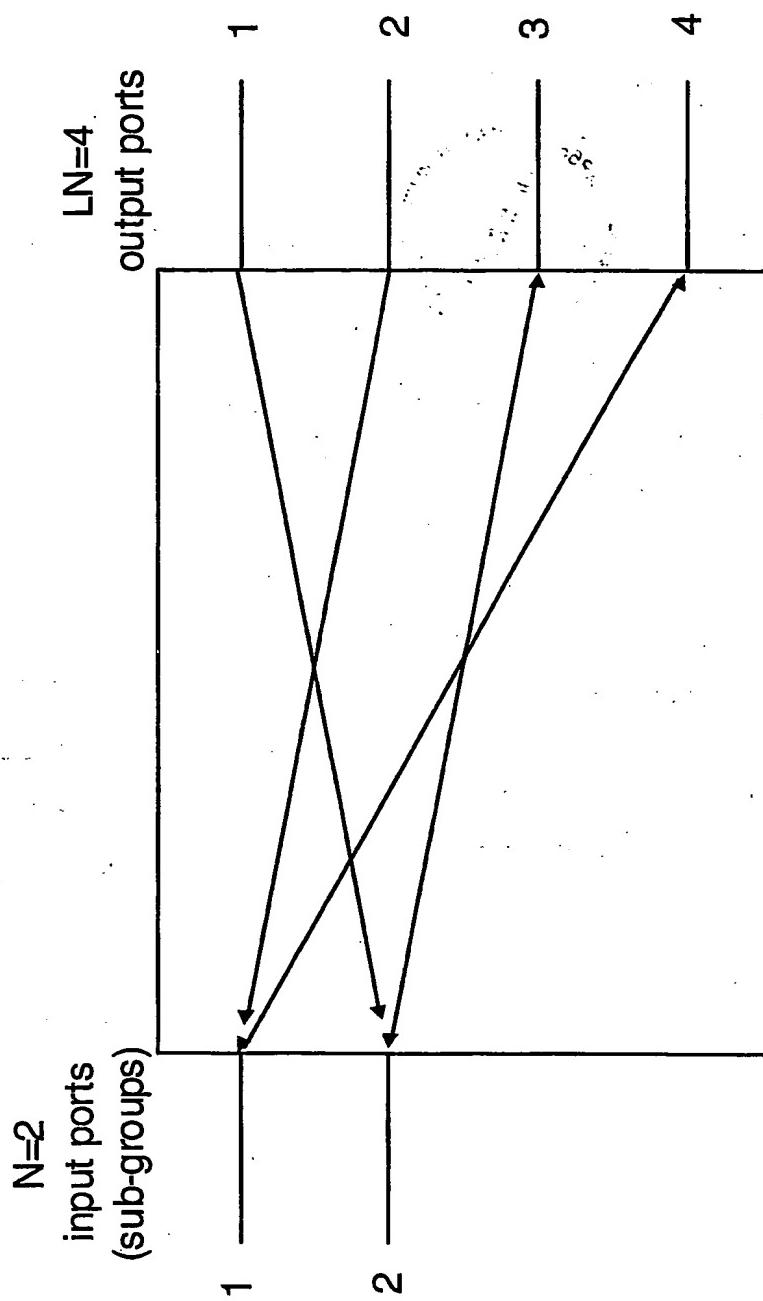


FIG 8